

A PROGRAMMABLE CONVERTER HAVING AN AUTOMATIC CHANNEL SEQUENCING MODE

(Figure 4)

A counter circuit is provided which is particularly suitable for controlling cyclical events. The counter consists of a chain of logic elements 160, 167, 164 which sequentially pass a '1' along the chain in response to a clock signal. Each element is also responsive to a respective select signal and, if selected, behaves like a latch, whereas if unselected it behaves as if it were not there.